

2N5086 (SILICON)

2N5087



MOTOROLA



CASE 29 (1)  
(TO-92)

PNP silicon annular transistors designed for low-level, low-noise amplifier applications.

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	50	Vdc
Collector-Base Voltage	$V_{CB}$	50	Vdc
Emitter-Base Voltage	$V_{EB}$	3.0	Vdc
Collector Current - Continuous Peak	$I_C$	50 100	mAdc
Total Device Dissipation @ $T_A = 25^\circ C$ Derate above $25^\circ C$	$P_D$	310 2.81	mW mW/ $^\circ C$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +135	$^\circ C$

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$\theta_{JA}$	0.357	$^\circ C/mW$

**ELECTRICAL CHARACTERISTICS**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector-Emitter Breakdown Voltage ( $I_C = 1.0 \text{ mA}$ , $I_B = 0$ )	$BV_{CEO}$	50	-	-	Vdc
Collector-Base Breakdown Voltage ( $I_C = 100 \mu\text{A}$ , $I_E = 0$ )	$BV_{CBO}$	50	-	-	Vdc
Collector Cutoff Current ( $V_{CB} = 10 \text{ Vdc}$ , $I_E = 0$ ) ( $V_{CB} = 35 \text{ Vdc}$ , $I_E = 0$ )	$I_{CBO}$	-	-	10 50	nAdc
Emitter Cutoff Current ( $V_{BE} = 3.0 \text{ Vdc}$ , $I_C = 0$ )	$I_{EBO}$	-	-	50	nAdc

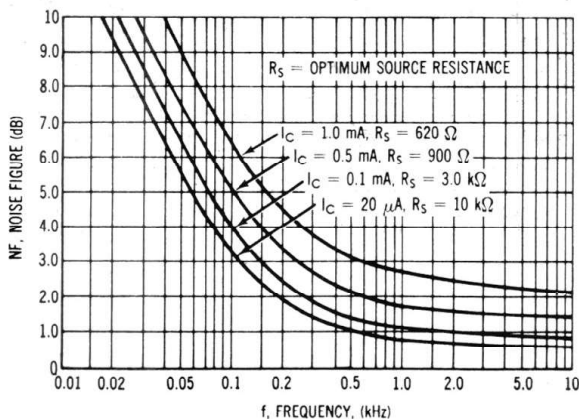
**ON CHARACTERISTICS**

DC Current Gain ( $I_C = 100 \mu\text{A}$ , $V_{CE} = 5.0 \text{ Vdc}$ ) ( $I_C = 1.0 \text{ mA}$ , $V_{CE} = 5.0 \text{ Vdc}$ ) ( $I_C = 10 \text{ mA}$ , $V_{CE} = 5.0 \text{ Vdc}$ )	2N5086 2N5087 2N5086 2N5087 2N5086 2N5087	$h_{FE}$	150 250 150 250 150 250	- - - - - -	500 800 - - - -	-
Collector-Emitter Saturation Voltage ( $I_C = 10 \text{ mA}$ , $I_B = 1.0 \text{ mA}$ )		$V_{CE(sat)}$	-	-	0.3	Vdc
Base-Emitter On Voltage ( $I_C = 1.0 \text{ mA}$ , $V_{CE} = 5.0 \text{ Vdc}$ )		$V_{BE(on)}$	-	-	0.85	Vdc

**DYNAMIC CHARACTERISTICS**

Current-Gain - Bandwidth Product ( $I_C = 500 \mu\text{A}$ , $V_{CE} = 5.0 \text{ Vdc}$ , $f = 20 \text{ MHz}$ )	2N5086 2N5087	$f_T$	40 40	120 150	- -	MHz
Output Capacitance ( $V_{CB} = 5.0 \text{ Vdc}$ , $I_E = 0$ , $f = 100 \text{ kHz}$ )		$C_{ob}$	-	-	4.0	pF
Small-Signal Current Gain ( $I_C = 1.0 \text{ mA}$ , $V_{CE} = 5.0 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	2N5086 2N5087	$h_{fe}$	150 250	- -	600 900	-
Noise Figure ( $I_C = 20 \mu\text{A}$ , $V_{CE} = 5.0 \text{ Vdc}$ , $R_S = 10 \text{ k ohms}$ , $f = 10 \text{ Hz to } 15.7 \text{ kHz}$ ) ( $I_C = 100 \mu\text{A}$ , $V_{CE} = 5.0 \text{ Vdc}$ , $R_S = 3.0 \text{ k ohms}$ , $f = 1.0 \text{ kHz}$ )	2N5086 2N5087 2N5086 2N5087	NF	- - - -	- - 1.2 1.0	3.0 2.0 3.0 2.0	dB

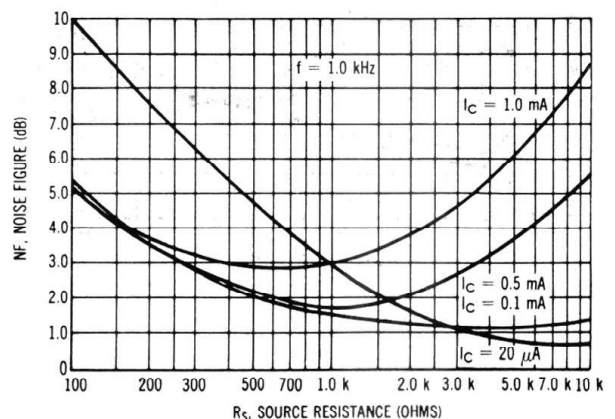
FIGURE 1 — FREQUENCY EFFECTS



**NOISE FIGURE**

$V_{CE} = 5.0 \text{ Vdc}$ ,  $T_A = 25$

FIGURE 2 — SOURCE RESISTANCE EFFECTS

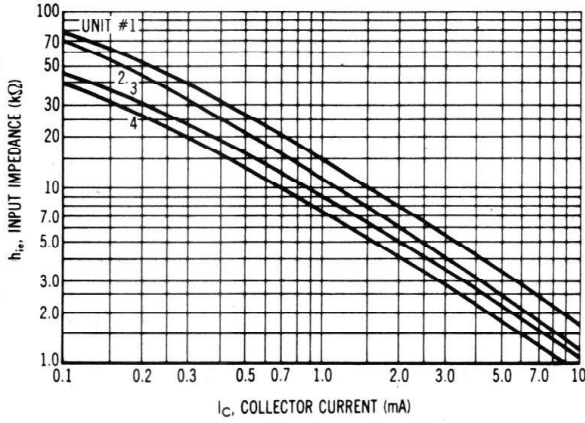


**h PARAMETERS**

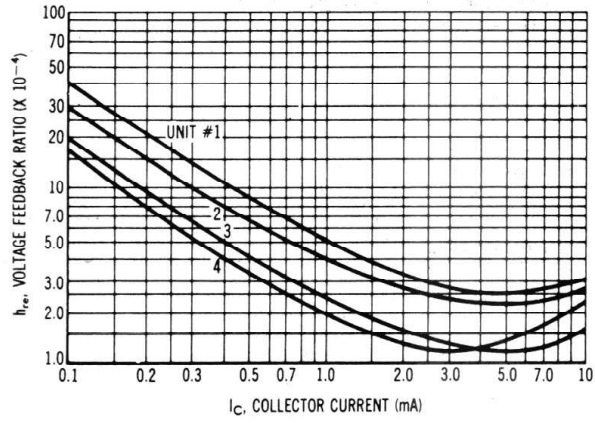
$V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$   
(For Figures 3, 4, 5, 6, 8)

This group of graphs illustrates the relationship of the "h" parameters for this series of transistors. To obtain these curves, 4 units were selected and identified by number — the same units were used to develop curves on each graph.

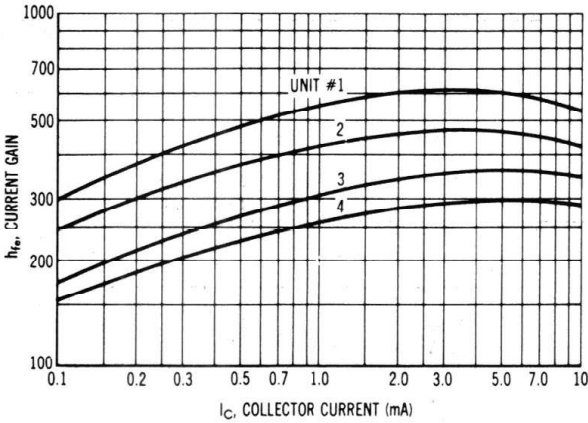
**FIGURE 3 — INPUT IMPEDANCE**



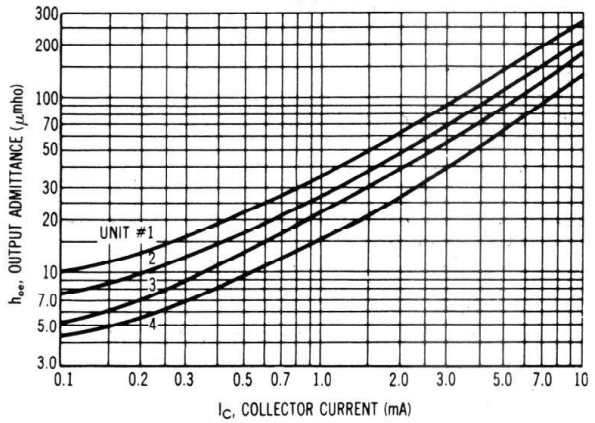
**FIGURE 4 — VOLTAGE FEEDBACK RATIO**



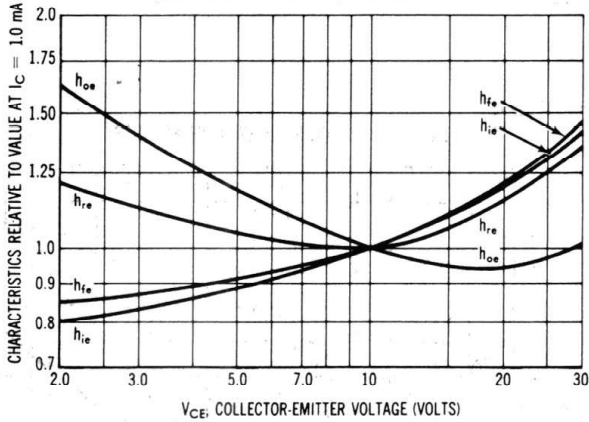
**FIGURE 5 — CURRENT GAIN**



**FIGURE 6 — OUTPUT ADMITTANCE**



**FIGURE 7 — EFFECT OF VOLTAGE**



**FIGURE 8 — DETERMINANT**

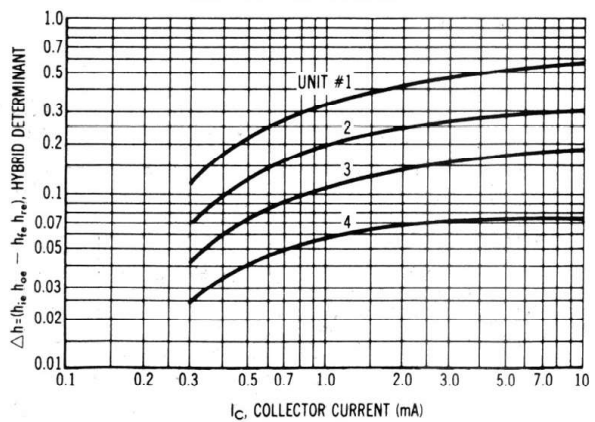


FIGURE 9 — DC CURRENT-GAIN

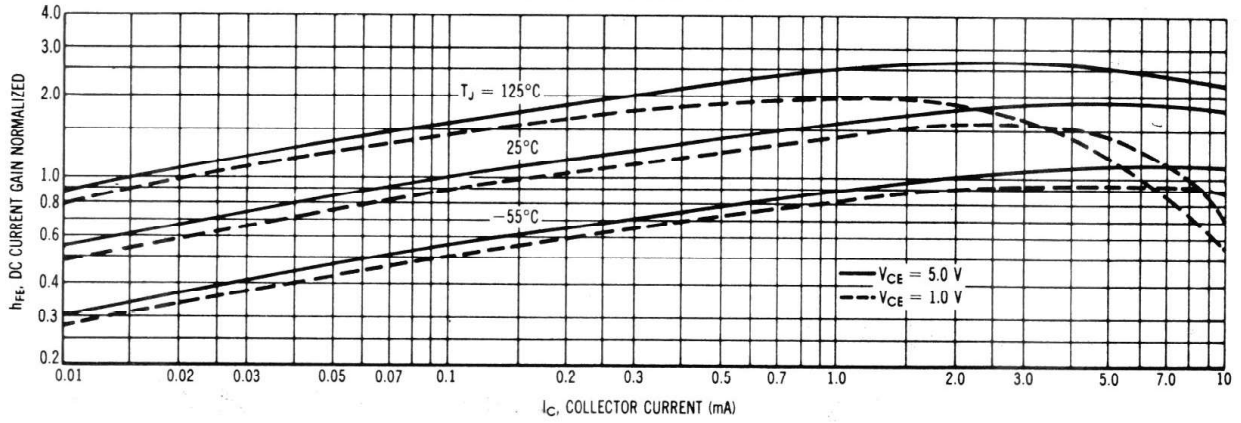


FIGURE 10 — COLLECTOR SATURATION REGION

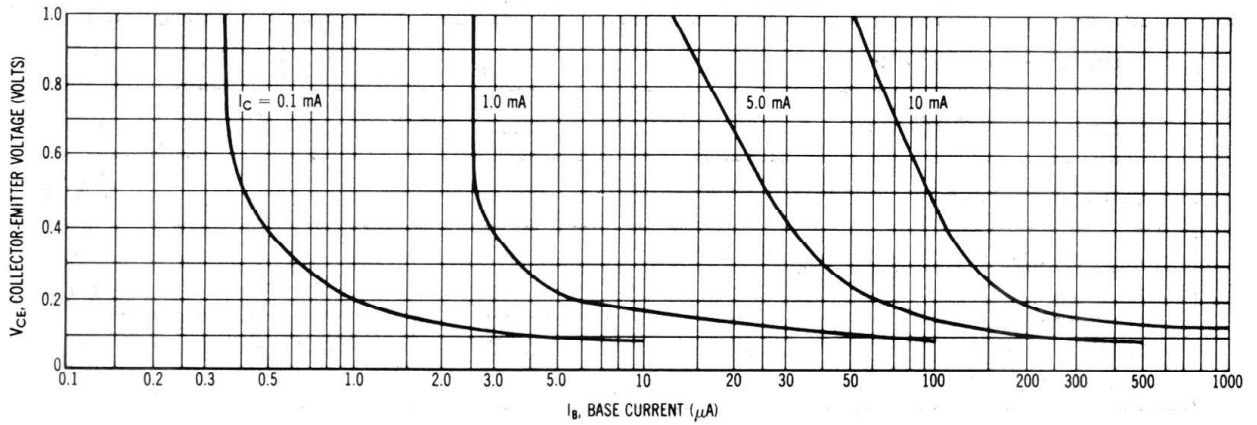


FIGURE 11 — CURRENT-GAIN — BANDWIDTH PRODUCT

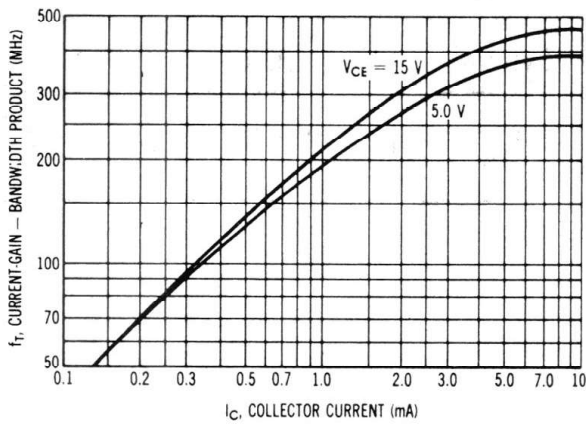


FIGURE 12 — CAPACITANCES

